

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	161	710/263.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/05 19:25
L8	7	710/266.ccls.	US-PGPUB	OR	ON	2006/06/05 19:25
L9	6	710/59.ccls.	US-PGPUB	OR	ON	2006/06/05 19:26
L10	13	I8 I9	US-PGPUB	OR	ON	2006/06/05 19:26
S1	49	("5513328" "5515481" "5587916" "5717840" "5907862" "6154785" "4574350" "4799153" "4807121" "5276813" "5367695" "5404347" "5416916" "5471675" "5513335" "5602650" "5602974" "5604713" "5664219" "5671365" "5677891" "5699516" "5717459" "5717850" "5749285" "5809334" "5815650" "5832204" "5832199" "5838777" "5845058" "5870766" "5875343" "5909544" "5922034" "5926628" "5956487" "5956344" "5991900" "6009504" "6032245" "6058478" "6061755" "6061755" "6105085" "6138198" "6141718" "6153927" "6167477" "6223230").pn.	US-PGPUB; USPAT	OR	ON	2006/06/05 12:56
S2	1413	pending near3 read	US-PGPUB; USPAT	OR	ON	2005/06/16 10:31
S3	700	(stall stalling halt) near3 write	US-PGPUB; USPAT	OR	ON	2005/06/16 10:32
S4	3	S2 with S3	US-PGPUB; USPAT	OR	ON	2005/06/16 10:32
S5	97	710/59.ccls.	US-PGPUB; USPAT	OR	ON	2005/06/16 14:54
S7	0	710/48-59	US-PGPUB; USPAT	OR	ON	2005/06/16 14:54
S8	3150	710/48-59.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/18 18:14
S9	33115	multiprocessor multi-processor (multiple adj processor)	US-PGPUB; USPAT	OR	ON	2005/06/16 15:44
S10	476	S8 and S9	US-PGPUB; USPAT	OR	ON	2005/06/16 14:55
S11	214621	stall stalling stalled wait halt	US-PGPUB; USPAT	OR	ON	2005/06/16 14:56
S12	284	S11 with S9	US-PGPUB; USPAT	OR	ON	2005/06/16 14:56

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S13	9	S12 and S8	US-PGPUB; USPAT	OR	ON	2005/06/16 14:57
S14	284	S9 with S11	US-PGPUB; USPAT	OR	ON	2005/06/16 15:45
S15	34	pending near3 device near3 reads	US-PGPUB; USPAT	OR	ON	2005/06/16 15:58
S16	0	interrupt with stall with until with read	US-PGPUB; USPAT	OR	ON	2005/06/16 15:58
S17	45	interrupt with S11 with until with read	US-PGPUB; USPAT	OR	ON	2005/06/20 11:21
S18	405	710/56.ccls.	US-PGPUB; USPAT	OR	ON	2005/06/17 14:10
S19	1	S18 and interupt	US-PGPUB; USPAT	OR	ON	2005/06/17 14:11
S20	121	S18 and interrupt	US-PGPUB; USPAT	OR	ON	2005/06/17 14:11
S21	3135	pending with read	US-PGPUB; USPAT	OR	ON	2005/06/17 15:15
S22	411	710/57.ccls.	US-PGPUB; USPAT	OR	ON	2005/06/17 15:16
S23	231935	stall halt pause wait	US-PGPUB; USPAT	OR	ON	2005/06/20 11:25
S24	1648360	complete completing completed	US-PGPUB; USPAT	OR	ON	2005/06/20 13:04
S25	2614028	device slave	US-PGPUB; USPAT	OR	ON	2005/06/20 11:26
S26	2	S23 with S24 with S25 with pending with read	US-PGPUB; USPAT	OR	ON	2005/06/20 13:02
S28	32	S23 with S24 with pending with read	US-PGPUB; USPAT	OR	ON	2005/06/20 13:05
S29	535647	buffer queue fifo	US-PGPUB; USPAT	OR	ON	2005/06/21 13:10
S30	0	devuce adj access adj S29	US-PGPUB; USPAT	OR	ON	2005/06/21 13:10
S31	42	device adj access adj S29	US-PGPUB; USPAT	OR	ON	2005/06/21 13:20
S32	49	bus adj request adj S29	US-PGPUB; USPAT	OR	ON	2005/06/22 17:49
S37	4157	bus adj request	US-PGPUB; USPAT	OR	ON	2005/06/23 13:14
S38	215251	stall stalling stalled wait halt	US-PGPUB; USPAT	OR	ON	2005/06/23 13:14
S39	806	S37 same S38	US-PGPUB; USPAT	OR	ON	2005/06/23 13:14

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S40	363	S37 with S38	US-PGPUB; USPAT	OR	ON	2005/06/23 13:15
S41	131	S37 near3 S38	US-PGPUB; USPAT	OR	ON	2005/06/23 13:42
S42	33284	multiprocessor multi-processor (multiple adj processor)	US-PGPUB; USPAT	OR	ON	2005/06/23 13:43
S43	3612	S42 and interrupt and pending	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S44	3414	S43 and device	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S45	3246	S44 and read	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S46	330	pending with device with read	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S47	506	pending with device with access	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S48	8378	S42 and interrupt	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S49	51	S48 and S47	US-PGPUB; USPAT	OR	ON	2005/06/23 13:44
S50	1	("6470408").PN.	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:02
S51	0	S50 and DMA	US-PGPUB; USPAT	OR	ON	2005/06/23 15:09
S52	1	S50 and PCI	US-PGPUB; USPAT	OR	ON	2005/06/23 15:10
S53	1	S50 and pending	US-PGPUB; USPAT	OR	ON	2005/06/23 15:10
S54	0	"forces all pending DMA"	US-PGPUB; USPAT	OR	ON	2005/06/23 15:11
S55	3	forces with pending with DMA	US-PGPUB; USPAT	OR	ON	2005/06/23 15:11
S56	78	"IA-32 processor"	US-PGPUB; USPAT	OR	ON	2005/06/23 20:00
S57	1	("6470408").PN.	US-PGPUB; USPAT	OR	OFF	2005/06/23 20:03
S58	1	S57 and interrupt	US-PGPUB; USPAT	OR	ON	2005/06/23 20:05
S59	1181	interrupt adj mode	US-PGPUB; USPAT	OR	ON	2005/06/23 20:05
S60	152	S59 with processor	US-PGPUB; USPAT	OR	ON	2005/06/23 20:05
S61	4	S60 same core	US-PGPUB; USPAT	OR	ON	2005/06/23 20:07

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S62	11	S60 with write	US-PGPUB; USPAT	OR	ON	2005/06/23 20:11
S63	57	S59 with write	US-PGPUB; USPAT	OR	ON	2005/06/23 20:39
S64	12	interrupt with stop with write with request	US-PGPUB; USPAT	OR	ON	2005/06/23 20:40
S65	237	710/266.ccls.	US-PGPUB; USPAT	OR	ON	2005/06/23 21:17
S66	2	"6253275"	US-PGPUB; USPAT	OR	ON	2005/06/23 21:17
S67	9259	@ad>"20050629"	US-PGPUB; USPAT	OR	ON	2006/01/18 18:15
S68	3354	710/48-59.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/18 18:15
S69	2	S67 and S68	US-PGPUB; USPAT	OR	ON	2006/01/18 18:15
S70	244138	@pd>"20050629"	US-PGPUB; USPAT	OR	ON	2006/01/18 18:15
S71	192	S70 and S68	US-PGPUB; USPAT	OR	ON	2006/01/18 18:15
S72	2	("6625679").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/05 09:09
S73	2	("6757775").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/05 17:40

WEST Search History

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DATE: Monday, June 05, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L15	l11.ab.	8
<input type="checkbox"/>	L14	l11.clm.	18
<input type="checkbox"/>	L13	L12 with (shared near2 (peripheral or i/o or memory))	1
<input type="checkbox"/>	L12	L4 with ((bridge or interfac\$4) near5 logic)	113
<input type="checkbox"/>	L11	L10 with bus	44
<input type="checkbox"/>	L10	L7 with (shared near2 (peripheral or i/o or memory))	100
<input type="checkbox"/>	L9	L8 with bus	151
<input type="checkbox"/>	L8	L7 with (peripheral or i/o)	497
<input type="checkbox"/>	L7	L4 with (bridge or interfac\$4)	3732
<input type="checkbox"/>	L6	L5 same (peripheral or i/o)	3346
<input type="checkbox"/>	L5	L4 same (bridge or interfac\$4)	9333
<input type="checkbox"/>	L4	(plurality or multiple) near2 processors	51121
<input type="checkbox"/>	L3	L2 same ((complet\$4 or execut\$4 or finish\$4) with (pending near2 (read or write or access)))	16
<input type="checkbox"/>	L2	(read or write or access) near2 (stall\$4 or ceas\$4 or stop\$4 or halt\$4)	13554
<input type="checkbox"/>	L1	(read or write or access) near5 (stall\$4 or ceas\$4 or stop\$4 or halt\$4)	30599

END OF SEARCH HISTORY